

Faculty CV

1. Name of Faculty: **Dr. Vinod Pralhad Tayade**
2. Present Position : **Lecturer (Senior Scale) in Electronics**
3. Address : **RH-4, Sai Nirmal DGP Nagar-1, Nashik.**
4. Mobile No. : **09766334676**
5. E-Mail Id : **taydevinod@gmail.com**
6. Date of Birth : **06/01/1982**



5. Memberships of professional organizations/Bodies :

- 1: Life Member of Indian Society of Technical Education (ISTE)
- 2: Student Membership of IEEE

QUALIFICATIONS:

Sr. No	Examination	Institute	Board/ University	% of Marks	Class Award	Remark
1.	Ph.D. (Electronics and Telecommunication Engineering)	AISSMS, Institute of Information Technology, Pune	Savitribai Phule Pune University, Pune	Completed	--	
1.	M.Tech (VLSI Design)	North Maharashtra University, Jalgaon	North Maharashtra University, Jalgaon	67.68%	First	
2.	BE E&Tc	Government College of Engineering, Jalgaon	North Maharashtra University, Jalgaon	67.5%	First	
3.	Diploma (Industrial Electronics)	SMIT Polytechnic, Jalgaon	MSBTE, Mumbai	74.95%	First	Institute Topper

ADDITIONAL SKILLS

1. Good programming skills.
2. Participating in research and development activities.
3. Master of Microcontroller and VLSI designing Subjects.
4. Expert of Semiconductor device design in Nanoscale regime.

CAREER DETAILS:

Sr. No.	Organization	Designation	Duration	Period	Work Done in Brief
1.	Government Polytechnic, Nashik	Lecturer (Senior Scale)	13.5 years	18-11-2010 to till date	Academic and administrative
2.	JSPM's Imperial College of Engineering and Research, Wagholi, Pune	Assistant Professor	3.5 Years	June-2007 to Nove-2010	Academic and administrative
3.	JointsChip Technologies Pvt. Ltd. Noida	Design Engineer	8 Months	Aug-06 to March-07	Designing and verification of IC.
4.	Shree Sureshdada Jain College of Enginnering, Jalgaon	Lecturer	1 Year	Aug-04 to Aug-05	Academic and administrative
5.	Jain Irrigation Systems Limited, Jalgaon	Supervisor	11 months	June-03 to May-04	Maintenance and Calibration of equipments.

SPECIALIZED TRAINING COURSES ATTENDED:

Sr. No.	Course Name	Duration	Place	Training Agency
1	The Cadence Virtuoso(R) Custom IC Design Flow	17/04/2006 to 21/04/2006	Banglore	Cadence Industry, Banglore
2	Induction Program Phase-I	30/05/2011 to 10/06/2011	GP Nashik	NITTTR, Bhopal GP Nashik
3	Advances in Signal & Image Processing (ASIP)	26/11/2012 to 30/11/2012	Jalgaon	TEQIP Phase II GCOE, Jalgaon
4	Recent Trends in Computer Networking (RTCN)	03/12/2012 to 07/12/2012	Jalgaon	TEQIP Phase II GCOE, Jalgaon
5	Induction Phase- II	22/07/2013 to 02/08/2013	GP Nashik	NITTTR, Bhopal GP Nashik
6	Personality Development through Positive thinking	03/03/2014 to 07/03/2014	GP Nashik	NITTTR, Bhopal GP Nashik
7	Nanotechnology and it's Applications	14/07/2014 to 18/07/2014	GP Nashik	NITTTR, Bhopal GP Nashik
8	Control System	02/12/2014 to 12/12/2014	Nashik	IIT Kharagpur, ISTE Workshop
9	VLSI & Embedded System Design	07/12/2015 to 11/12/2015	NITTTR, Bhopal	NITTTR, Bhopal
10	Control system - Analysis & Design using MATLAB	06/06/2016 to 17/06/2016	NITTTR, Kolkata	NITTTR, Kolkata
11	MATLAB and its Hardware interface	28/11/2016 to 02/12/2016	NITTTR, Chandigarh	NITTTR, Chandigarh
12	LabView & VHDL applications in Engineering	05/06/2017 to 09/06/2017	NITTTR, Kolkata	NITTTR, Kolkata
13	3D Animation	11/12/2017 to 15/12/2017	NITTTR, Bhopal	NITTTR, Bhopal
14	Microprocessor & Embedded System	04/06/2018 to 08/06/2018	NITTTR, Kolkata	NITTTR, Kolkata

15	Accreditation of Engineering Program	02/07/2018 to 06/07/2018	NITTTR, Bhopal	NITTTR, Bhopal
16	Hardware Modelling Using Verilog	Aug to Oct-18	NPTEL-AICTE FDP (Online)	NPTEL-AICTE FDP
17	Yashada – Refresher course	17/06/2019 to 21/06/2019	Yashada, Pune	Yashada, Pune
18	Low Power VLSI Design	20/04/2020 to 24/04/2020	NITTTR Chandigarh (Online mode)	NITTTR Chandigarh (Online mode)
19	Academic leadership enhancement in technical institutions	04/05/2020 to 08/05/2020	NITTTR Chandigarh (Online mode)	NITTTR Chandigarh (Online mode)
20	8051 and its applications	18/05/2020 to 22/05/2020	NITTTR Chandigarh (Online mode)	NITTTR Chandigarh (Online mode)
21	Research Trends in VLSI design	25/05/2020 to 29/05/2020	NITTTR Chandigarh (Online mode)	NITTTR Chandigarh (Online mode)
22	Nanotechnology for electronic and photonic devices	15/06/2020 to 19/06/2020	PEC, Chandigarh TEQIP-III (Online)	PEC, Chandigarh TEQIP-III
23	CMOS digital VLSI design	Jan-Apr-2020	NPTEL (Online)	NPTEL
24	Recent trends in Biomedical signal processing	23/11/2020 to 28/11/2020	AICTE FDP by Sipana college, Amaravati (Online)	AICTE FDP by Sipana college, Amaravati
25	Research and Quality Publication : Practices and Tools	20/12/2020 to 24/12/2020	TEQIP-III Central Library, COEP and MTU, Imphal (Online)	TEQIP-III Central Library, COEP and MTU, Imphal
26	Design and Fabrication of VLSI circuits	01/03/2020 to 05/03/2020	TEQIP-III, GEC Raipur (MP) (Online)	TEQIP-III, GEC Raipur (MP)
27.	Two weeks Industrial Training on IIOT	01/06/2021 to 15/06/2021	NSIC Hyderabad	NSIC Hyderabad
28	Industrial Training in Artificial Intelligence & Machine Learning using Python.	24/06/2021 to 08/07/2021	NSIC Hyderabad	NSIC Hyderabad
29	Advances in Internet of Things (IOT) and their applications	22/11/2022 to 25/11/2022	G P Awasari	MSBTE Mumbai
30	Design and development of microstrip patch antenna and wearable antennas for wireless communication.	26/02/2024 to 01/03/2024	G P Nashik	DTE Mumbai

RESEARCH ACTIVITIES

A. PhD Thesis - Design of Graphene based field effect transistor for variability immune low power applications.

In this research work a Silicon on Insulator Graphene Channel Field Effect Transistor (SIGFET) is designed using 22 nm technology. It is implemented using MATLAB and

Silvaco TCAD tool. This device has low power consumption and it is a perfect candidate to replace conventional MOSFET in Nanoscale regime.

B. Papers published in Refereed Journal Articles (Total 02)

1. **V. P. Tayade** and S. L. Lahudkar, "Implementation of 20 nm Graphene Channel Field Effect Transistors Using Silvaco TCAD Tool to Improve Short Channel Effects over Conventional MOSFETs", *Advances In Technology innovation*, vol. 7, no. 1, pp. 18-29, Oct. 2021. <https://doi.org/10.46604/aiti.2021.8098>.
2. **Tayade VP**, Lahudkar SL. Design of silicon-on-insulator field-effect transistor using graphene channel to improve short channel effects over conventional devices. *International Journal of Advanced Technology and Engineering Exploration* 2023; 10(101):458-470. DOI:10.19101/IJATEE.2021.876392.

C. Presentations and Publications in Refereed International Conference Proceedings (Total 02)

1. **V. Tayade** and S. Lahudkar, "A Review of Emerging Devices Beyond MOSFET for High Performance Computing," 2020 International Conference on Emerging Smart Computing and Informatics (ESCI), Pune, India, 2020, pp. 34-38, doi: 10.1109/ESCI48226.2020.9167594.
2. **V. P. Tayade** and S. L. Lahudkar, "Improvement of Short Channel Parameters using Double Gate Graphene Channel Field Effect Transistor," 2022 IEEE Delhi Section Conference (DELCON), 2022, pp. 1-5, doi: 10.1109/DELCON54057.2022.9753064.

WORKSHOP/ COMPETITION ORGANIZED/ARRANGED

1. Arduino Workshop developed by IIT Mumbai, conducted for faculty and students.
2. Pilot Workshop on OpenPLC organized for the regional level faculties, conducted by IIT Mumbai.
3. Worked as a Reviewer for IEEE International Region 10 Symposium TENSYP 2022.
4. Coordinator for District Level Online the Research Paper Competition organized on the occasion of the 75th Anniversary of Indian Independence.
5. Worked as an expert judge for the Paper presentation competition at MET Polytechnic, Nashik.
6. Industrial Visits of Third-year students organized at Nashik, Pune, and Aurangabad in different academic years.
7. Various expert lectures of electronics and telecommunication students organized in offline and online modes in different academic years.

EXPERT LECTURES/TALKS

1. Delivered an Introductory Lecture on “Robotics“ to the Students at Clifford International School Nashik.
2. Delivered an Expert lecture on “Microcontroller 8051” to the Diploma students of JMCT Polytechnic, Nashik.

WORK DONE AT INSTITUTE/ORGANIZATIONAL LEVEL

1. Developed Lab Manuals of courses Microcontroller, VLSI Technology, and Introduction to IT System.
2. Development of Microcontroller kits for practical at the institute level.
3. Guiding various multidisciplinary projects at the institute level.
4. Additional Officer in charge of Local Assessment Center, and Theory Examination at G P Nashik.
5. Involved in various monitoring/inspection committees assigned by Regional Office Nashik, RBTE C. Sambhaji Nagar (Aurangabad), DTE-Mumbai, MSBTE Mumbai.
6. Training and Placement coordinator for the Electronics and telecommunication department.
7. Seminar and Project Coordinator for the Electronics and telecommunication department.
8. Coordinator of industrial internship of diploma students. Placed 126 students in 34 different industries of Nashik.

WORK DONE FOR SOCIETY

1. Conducted Oxygen Cylinder Inspection at various hospitals in Nashik during COVID 19 period. Hon. Collector office Nashik assigned the activity.
2. Performing duties as Sub-Centre Chief in conducting MPSC, UPSC, and SSC examinations assigned by Hon. Collector office Nashik.
3. Active participant in school connect program to create awareness among students about diploma engineering education.
4. Performing duties in conduction of various general and constituency elections of MLP and MPs assigned by Election commission of India Via. Hon. Collector office Nashik.

SIGNATURE

Dr. Vinod Pralhad Tayade